

FIG. 1

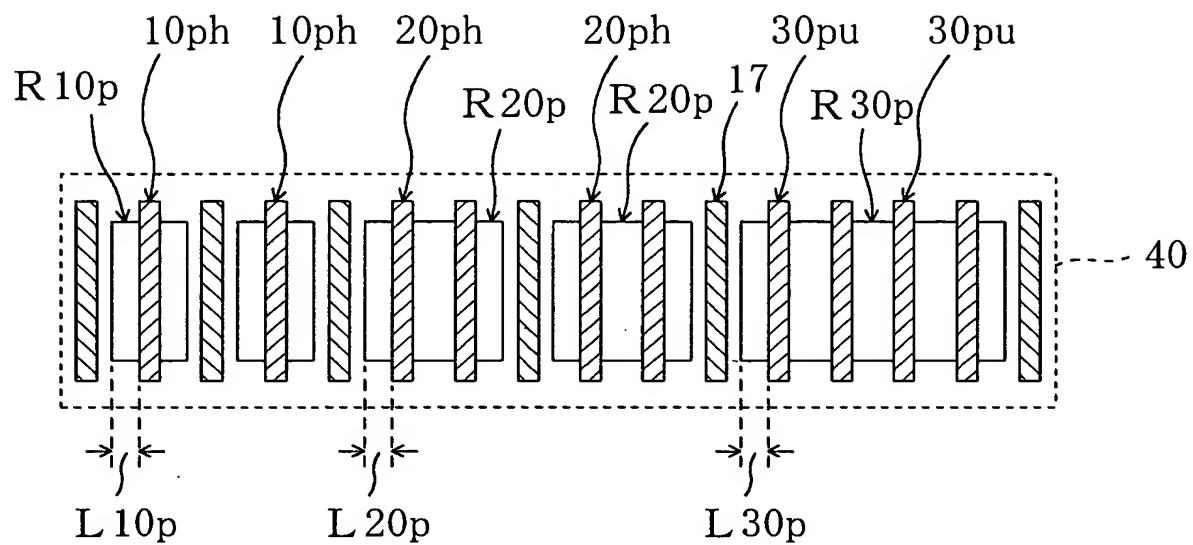


FIG. 2

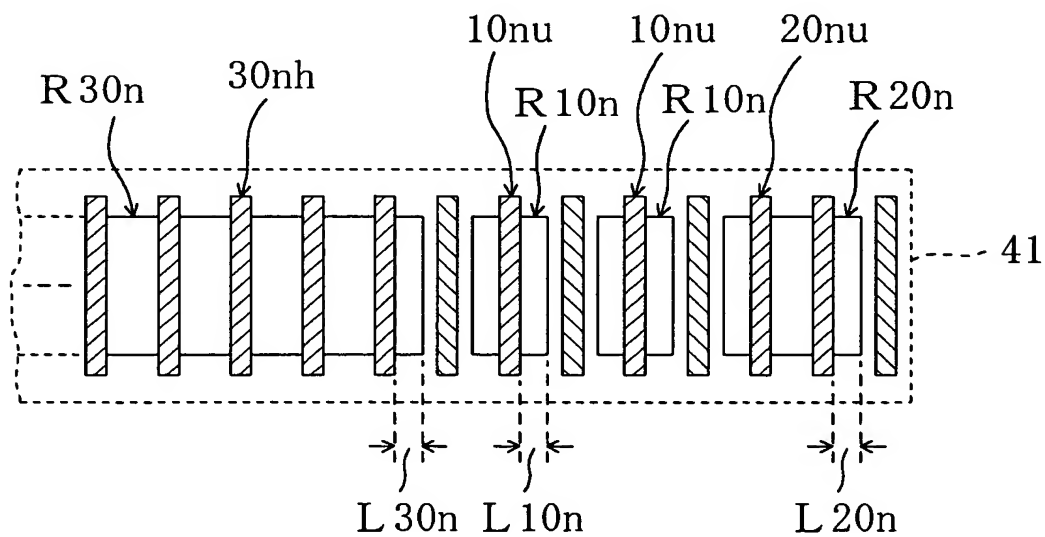


FIG. 3

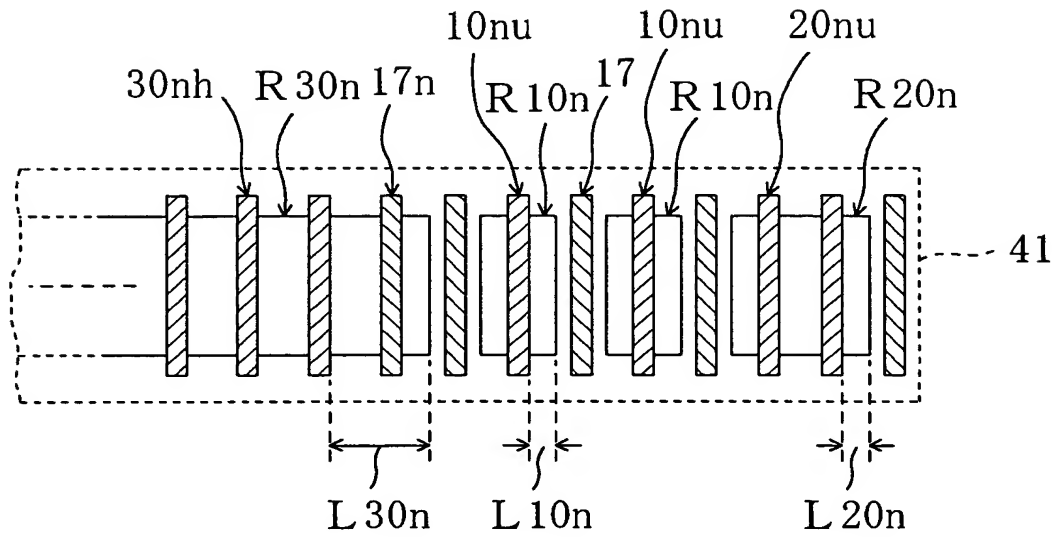


FIG. 4

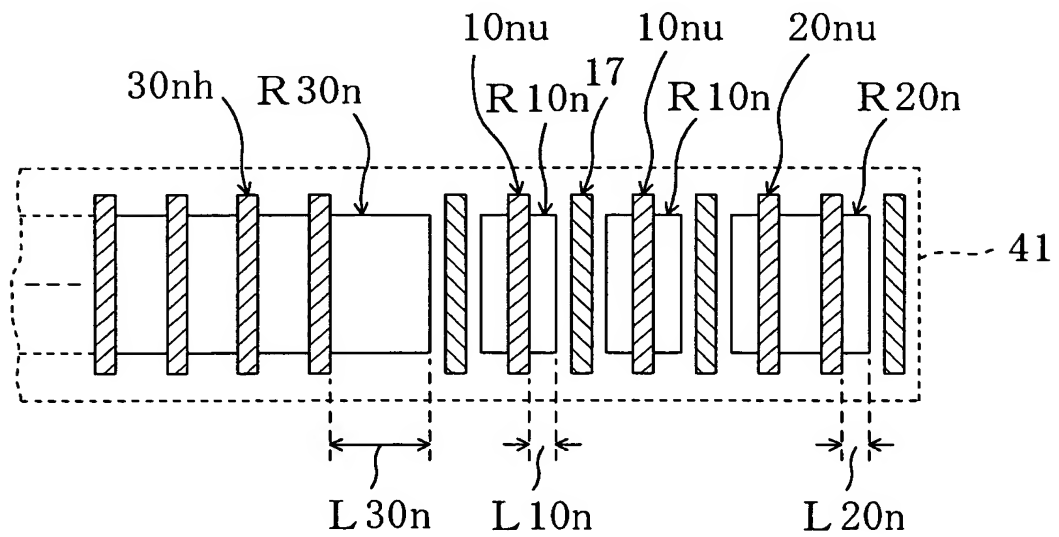


FIG. 5

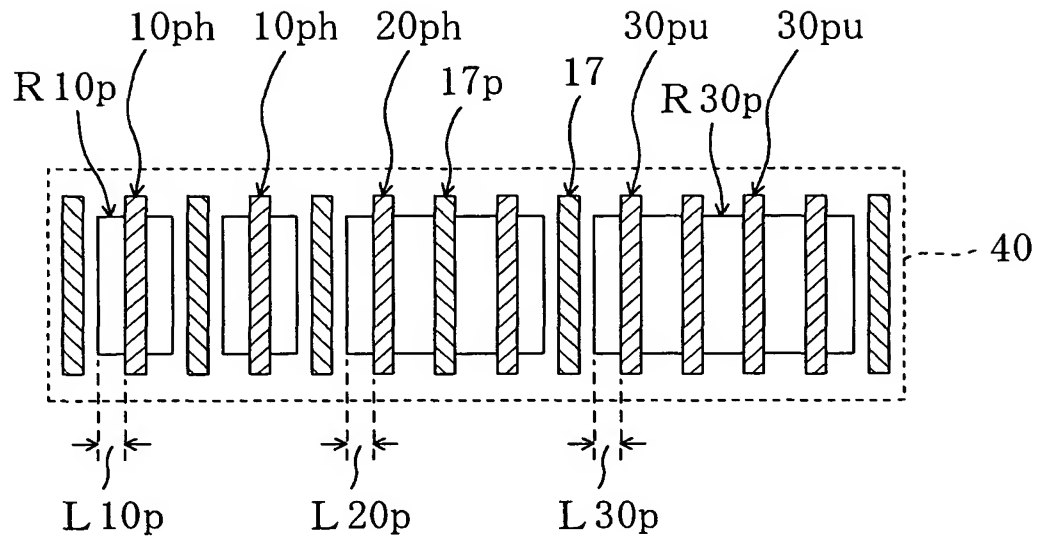


FIG. 6A

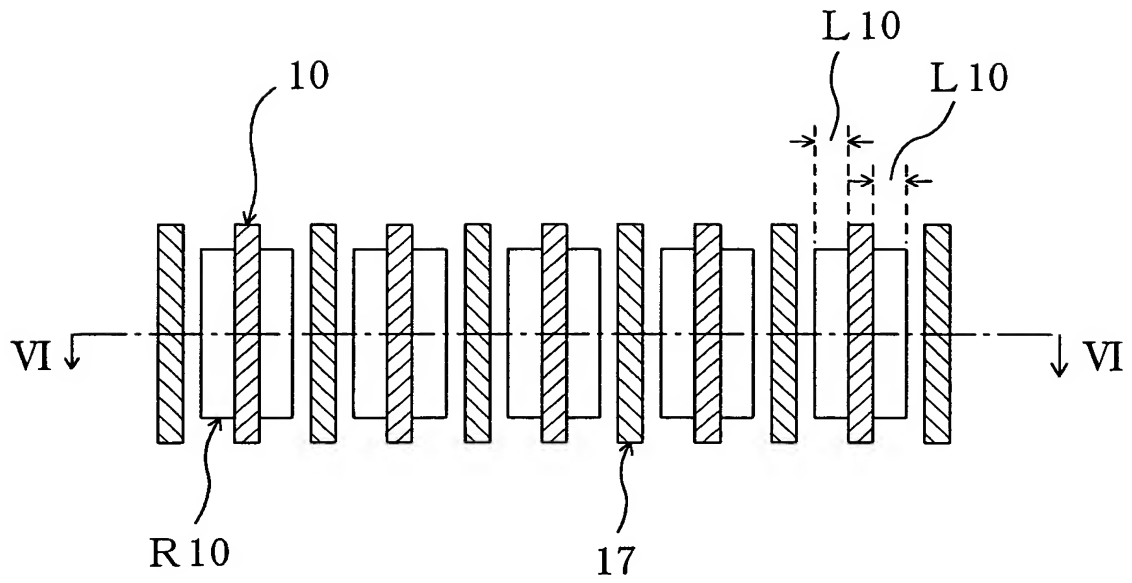


FIG. 6B

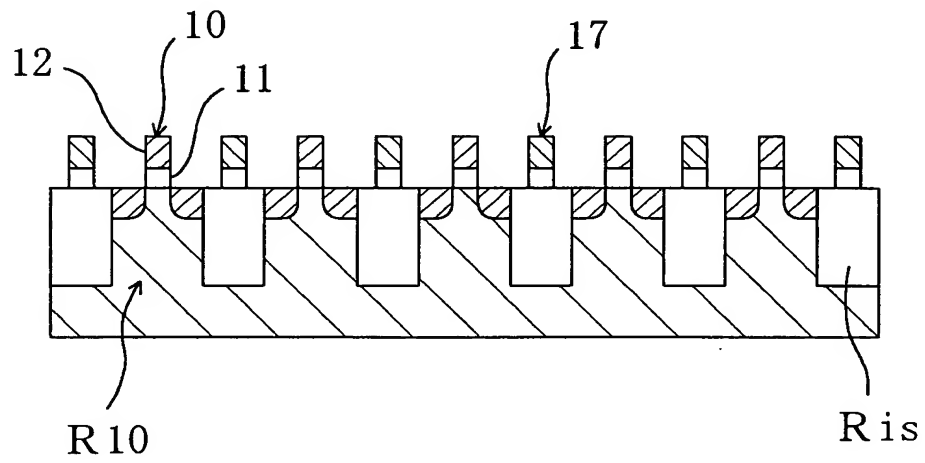


FIG. 7A

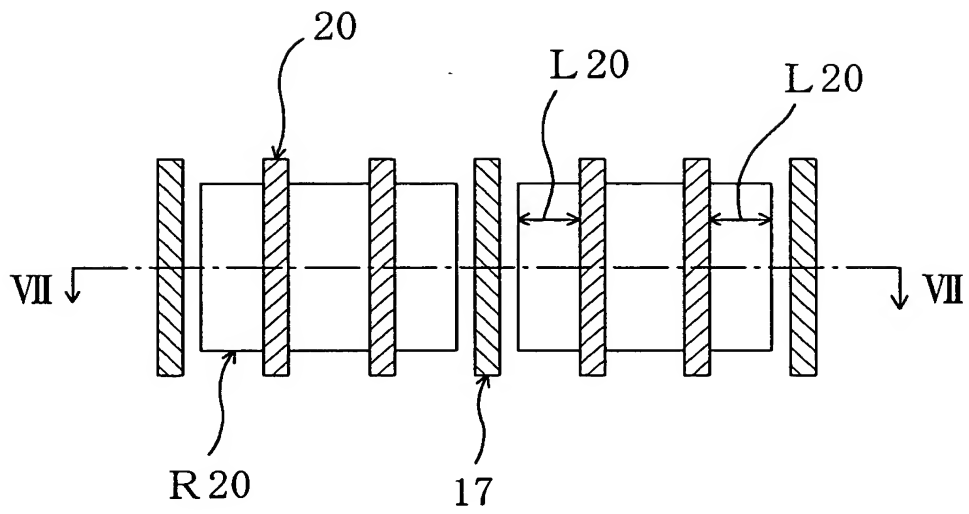


FIG. 7B

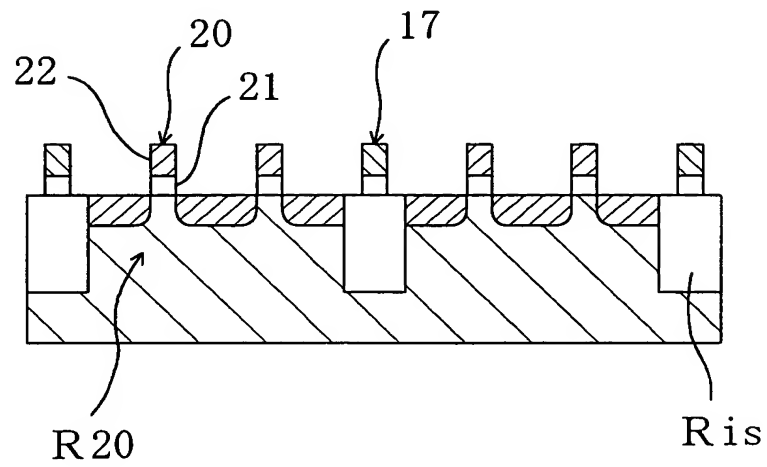


FIG. 8A

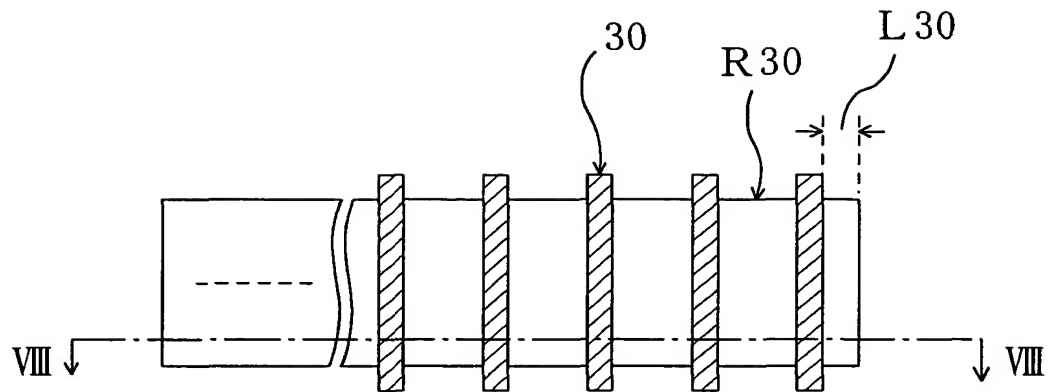


FIG. 8B

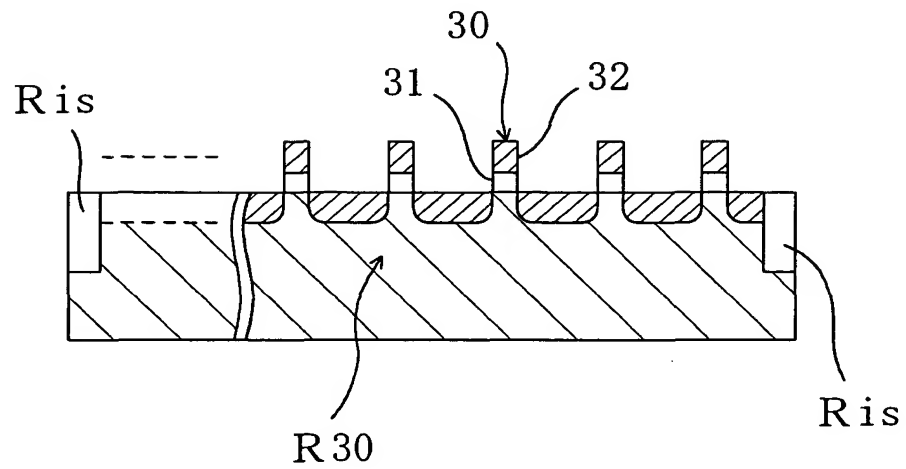


FIG. 9

 $L_g = 0.15 \mu\text{m}$   $W_g = 0.4 \mu\text{m}$ 

		pattern			
		(a)	(b)	(c)	(d)
PMISFET	Idsat ratio	1.00	0.95	0.94	0.94
NMISFET	Idsat ratio	1.00	1.11	1.10	1.03

FIG. 10A

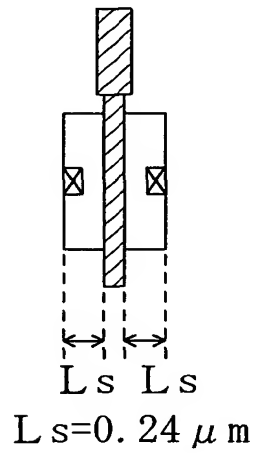


FIG. 10B

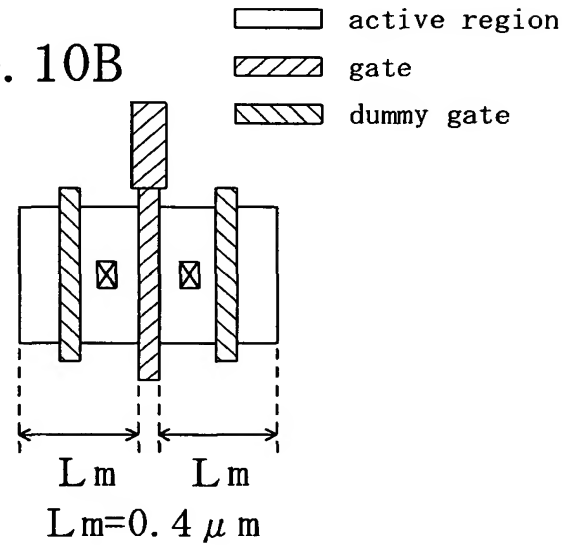


FIG. 10C

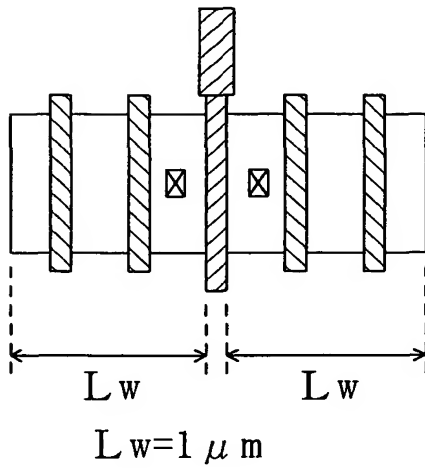


FIG. 10D

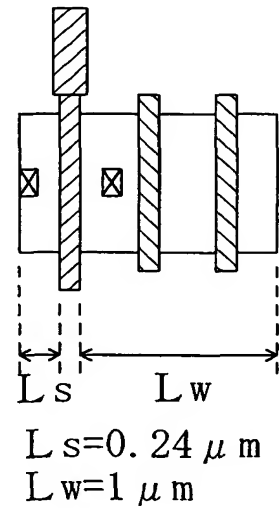


FIG. 11  
PRIOR ART

